ECE 351

Professor Greenwood “somebody who doesn’t know the answer tell me…”

04/18/19

* Only gate and dataflow levels of abstraction will be on the 1st test.
* **Timing Controls**

**parameter** on\_delay = 3, off\_delay = 5;

**reg** A;

**initial** A = 0;

**always**

begin

#on\_delay A = 1;

#off\_delay A = 0;

end

A: \_\_\_\_\_\_\_|’’’’’’’’’’’’’’’’’’’’’’’’|\_\_\_\_\_\_\_\_\_\_\_\_

t: 0 3 8 -> t

case 1:

**initial**

**begin**

r = 0;

#100 re =1